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Fault-Tolerant and Testable Computing Systems. Fall 2019. Professor Daniel J. Sorin. Course Objective and Content. Objective: To provide students with an understanding of fault tolerant computers, including both the theory of how to design and evaluate them and the practical knowledge of real fault tolerant systems.

ECE/CS 554 - Fault-Tolerant and Testable Computing Systems

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The next two chapters present various methods for fault-tolerant hardware design and procedures for implementing self-checking and fail-safe circuits. Chapter 6 focuses on design for testability; it describes various design techniques which can be used to simplify the testing procedures for digital circuits.

Fault tolerant and fault testable hardware design | Guide ...

Fault Tolerant and Fault Testable Hardware Design by Lala Parag K. (1984-12-01) Hardcover on Amazon.com. *FREE* shipping on qualifying offers. Fault Tolerant and Fault Testable Hardware Design by Lala Parag K. (1984-12-01) Hardcover

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Fault tolerance is the property that enables a system to continue operating properly in the event of the failure of (or one or more faults within) some of its components. If its operating quality decreases at all, the decrease is proportional to the severity of the failure, as compared to a naively designed system, in which even a small failure can cause total breakdown.

Fault tolerance - Wikipedia

Fault tolerant systems have the capability of withstanding defects and are able to provide specified output despite faults occurring or having occurred. Similarly design for testability (DFT) is a technique that facilitates ease of testing of complicated electronic systems.

Defect/Fault Tolerant Systems and Design for Testability ...

Fault-Tolerant and Testable Computing Systems. Fall 2011. Professor Daniel J. Sorin. Course Objective and Content. Objective: To provide students with an understanding of fault tolerant computers, including both the theory of how to design and evaluate them and the practical knowledge of real fault tolerant systems.

ECE 254 / CPS 225 - Fault-Tolerant and Testable Computing ...

However, a practical FFT chip is normally very big, so effective testing and fault-tolerance techniques usually are required. In this paper, we first propose a C-testable FFT network design.

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