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Appendix C Solutions. Again we have 99 iterations. There are two RAW stalls and a flush after the branch since the branch is taken. The total number of cycles is  $9 \cdot 98 + 12 = 894$ . The last loop takes three addition cycles since this latency cannot be overlapped with additional loop instances. d. See the table below. LD. R1, 0(R2) DADDI R1, R1, #1 SD. R1, 0(R2)

### Appendix c | Integrated Circuit | Computer Programming

Reading Assignment: Appendix C, sections C.1 and C.2 ; Lecture of February 7: Recap: Pipelining (II) Reading Assignment: Appendix C, sections C.4, C.5 and C.7; Lecture of February 12: Recap for the 1st quiz Here is the solution to the exercise posted in the 3rd lecture. Here are the solutions to the exercises discussed today.

### COSC 6385 Computer Architecture, Spring 2013

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1 Solutions to Exercises for Appendix E in Computer Architecture: A Quantitative Approach, 4th Edition c circlecopyrt Wai Hong Ho and Timothy Mark Pinkston SMART Interconnects Group University of Southern California E-1 Refer to “Computer Architecture: A Quantitative Approach (3rd Edition)” Chapter 8 solutions for Problem 8.9. E-2 Refer to “Computer Architecture: A Quantitative Approach ...

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Chapter 3 and Appendix C of the Textbook. HW-3 . 10/17. Instruction-Level Parallelism and Its Exploitation (reference slides from past) Chapter 3 and Appendix C of the Textbook . 10/19. Review of Chapters 1, 2, and Appendix C. Chapter 3 and Appendix C of the Textbook . 10/22. In-Class Exam I, covering Chapters 1, 2, and Appendix C. Open book ...

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Karthik Desingh. Karthik Desingh is a postdoctoral scholar in Paul G. Allen School of Computer Science & Engineering at the University of Washington, working with Prof. Dieter Fox in the Robotics and State Estimation Lab. He completed his Ph.D. in Computer Science and Engineering from the University of Michigan under the supervision of Prof. Chad Jenkins.

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